



Displays 32 bytes in R0-RF in 7-seg LEDs as 4 frames (short P5 for 2) of 8 bytes, 1 frame/sec, and repeats.

- Fetch (SC0=0) makes GLO Rn, GHI Rn instructions (opcodes 8n, 9n) from address, VCC, and GND.
 - repeat GLO R0-R7 128 times,
 - repeat GLO R8-RF 128 times,
 - repeat GHI R0-R7 128 times,
 - repeat GHI R8-RF 128 times, & loop
- Execute (SC0=1) Rn.0 or Rn.1 on bus drives LED anodes. U3 decodes n to select 1 LED cathode. 1802 WAITS 1 msec when (Execute AND TPB)=1 so LEDs visible.
- U2 used as address latch.
- Load with Membership Card, PC Parallel Port, or Load Box below.

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Title 1802 Name Tag with WAIT states		
Size	Document Number	REV
A	C:\ORCAD\SHEET\1802\NAME18-3.SCH	A
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